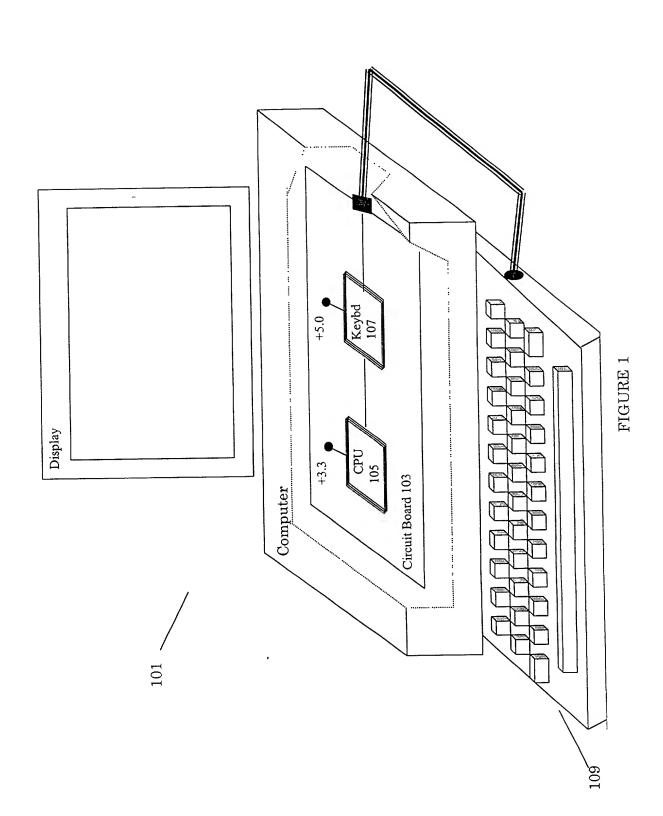
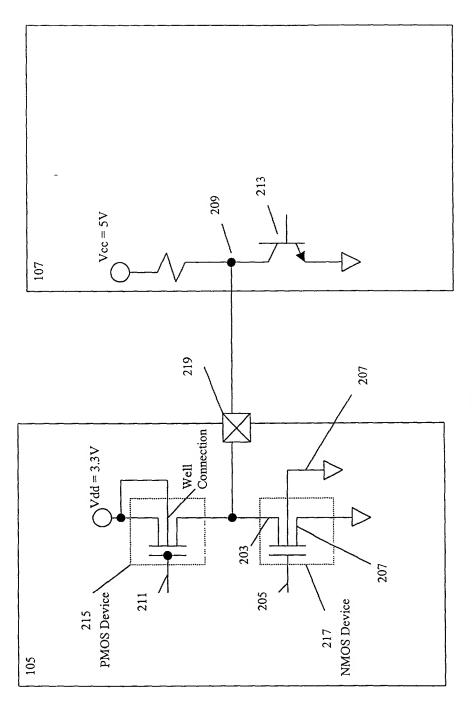
Docket No.: 41980/NJP/B600
Inventor(s): Janardhanan S Ajit .
Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT INPUT OUTPUT (I/O) CIRCUIT WHICH ACCOMMODATES LARGE POWER SUPPLY VARIATIONS
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Docket No.: 41980/NJP/B600
Inventor(s): Janardhanan S Ajit .
Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT INPUT OUTPUT (I/O) CIRCUIT WHICH ACCOMMODATES LARGE POWER SUPPLY VARIATIONS
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Prior Art

Figure 2

Docket No.: 41980/NJP/B600
Inventor(s): Janardhanan S. Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT
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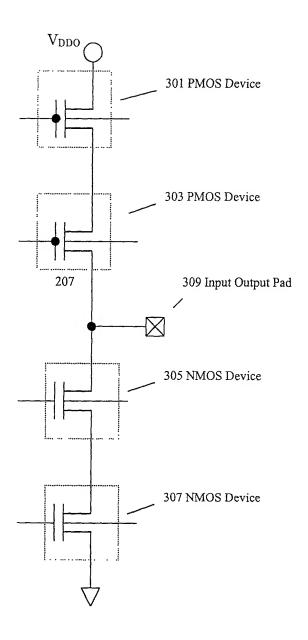


Figure 3

Attorney: Nicholas J. Pauley
Docket No.: 41980/NJP/B600
Inventor(s): Janardhanan S Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT
INPUT OUTPUT (I/O) CIRCUIT WHICH ACCOMMODATES
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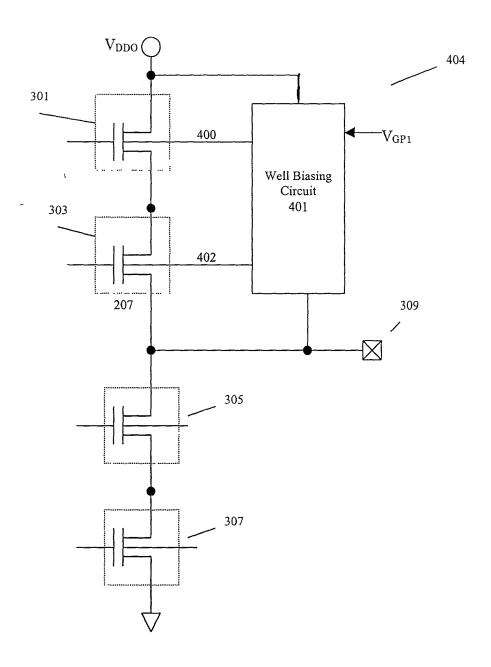


Figure 4

Attorney: Nicholas J. Pauley
Docket No.: 41980/NJP/B600
Inventor(s): Janardhanan S Ajit .
Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT
INPUT OUTPUT (I/O) CIRCUIT WHICH ACCOMMODATES
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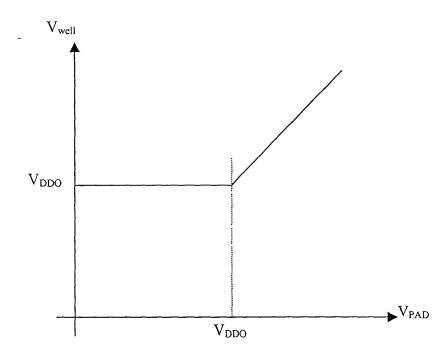


Figure 5

Nicholas J. Pauley 41980/NJP/B600 Attorney: Docket No.: Inventor(s): Janardhanan S Ajit

Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT INPUT OUTPUT (I/O) CIRCUIT WHICH ACCOMMODATES LARGE POWER SUPPLY VARIATIONS

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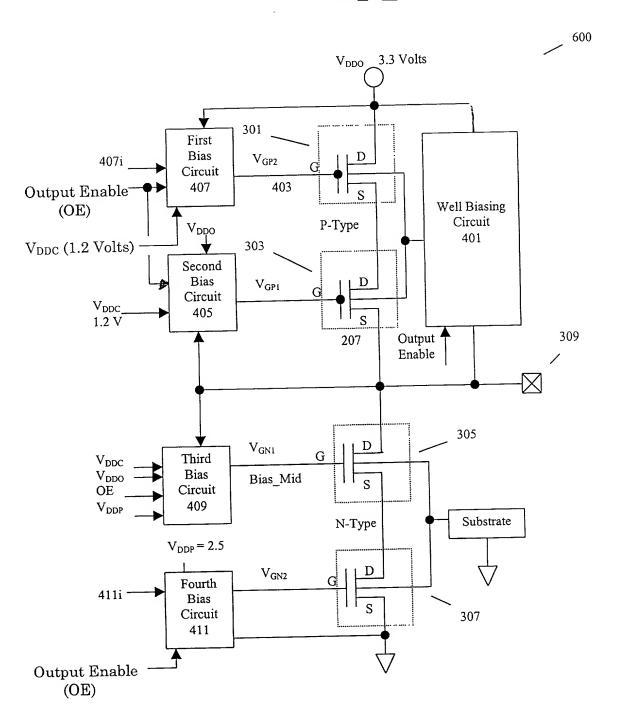


Figure 6

Attorney: Nicholas J. Pauley
Docket No.: 41980/NJP/B600
Inventor(s): Janardhanan S. Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT
INPUT OUTPUT (I/O) CIRCUIT WHICH ACCOMMODATES
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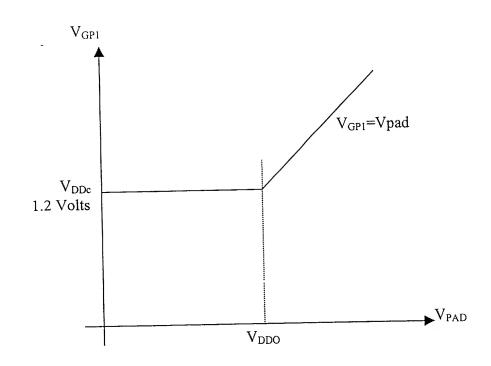


Figure 7

Attorney: Nicholas J. Pauley
Docket No.: 41980/NJP/B600
Inventor(s): Janardhanen S. Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT
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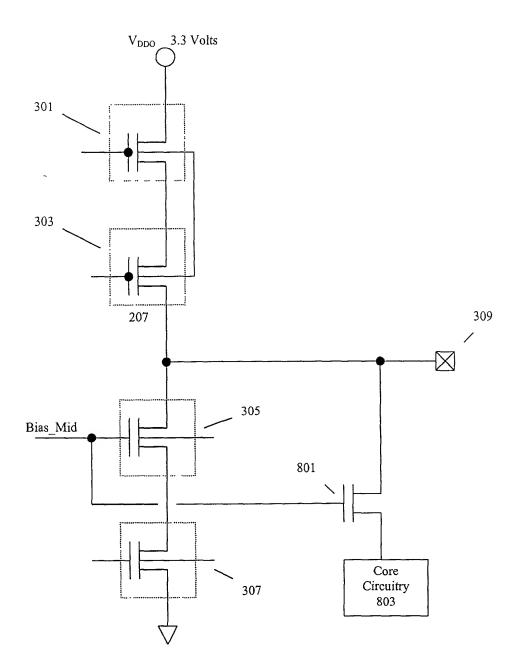


Figure 8

Attorney: Nicholas J. Pauley
Docket No.: 41980/NJP/B600
Inventor(s): Janardhanan S Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT
INPUT OUTPUT (I/O) CIRCUIT WHICH ACCOMMODATES
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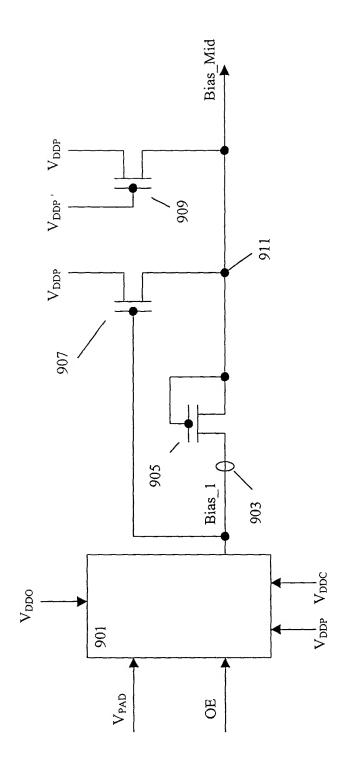
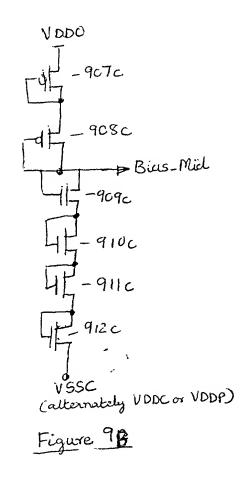


Figure 9A

Attorney: Nicholas J. Paulley
Docket No.: 41980/NJP/B600
Inventor(s): Janardhanan S. Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT
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Attorney: Nicholas J. Pauley
Docket No.: 41980/NJP/B600
Inventor(s): Janardhanan S 'Ajıt
Title: SUB-MICRON HIGH INPUT JOLTAGE, TOLERANT
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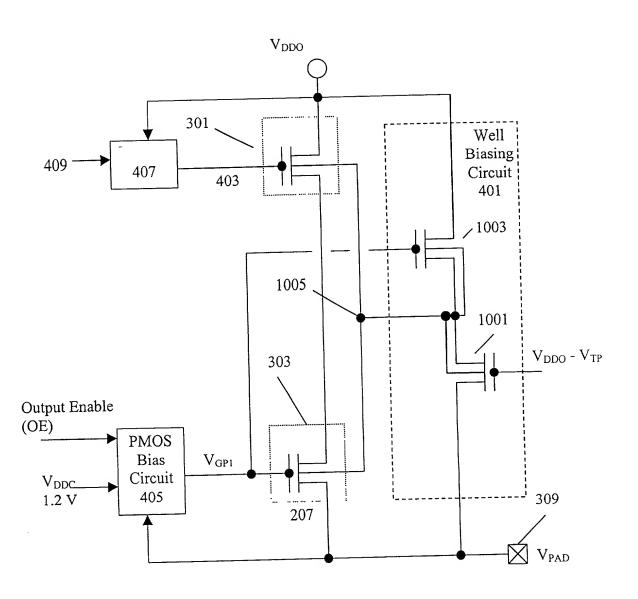


Figure 10

Attorney: Nicholas J. Pauley
Docket No.: 41980/NJP/B600
Inventor(s): Janardhanan S. Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT
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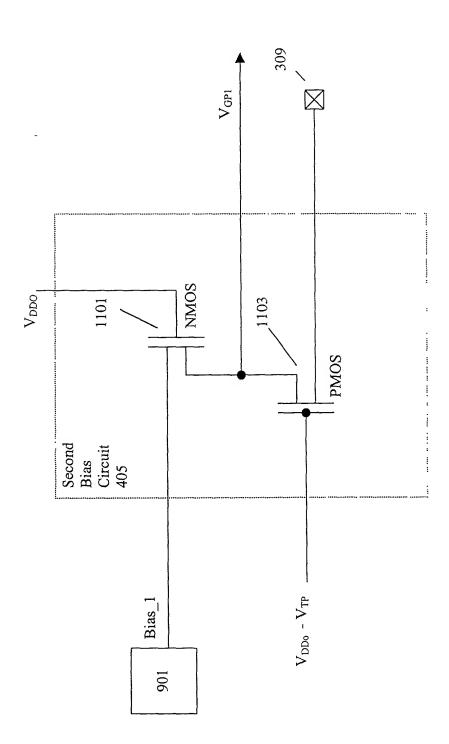


Figure 11A

Attorney: Nicholas J. Pauley
Docket No.: 41980/NJP/B800
Inventor(s): Janardhanan S. Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT
INPUT OUTPUT (I/O) CIRCUIT WHICH ACCOMMODATES
LARGE POWER SUPPLY VARIATIONS
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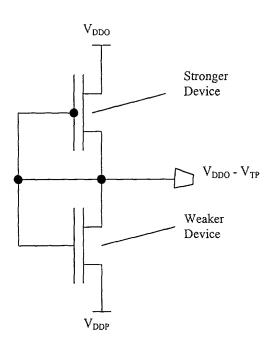


Figure 11B

Attorney: Nicholas J. Pauley
Docket No.: 41980/NJP/B600
Inventor(s): Janardhanan S Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT
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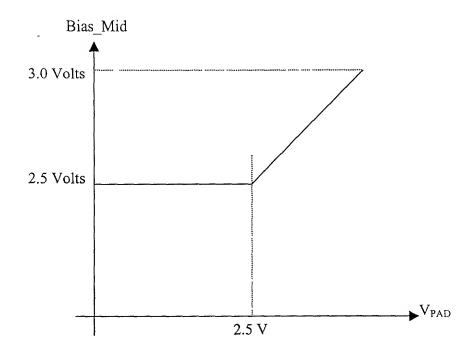
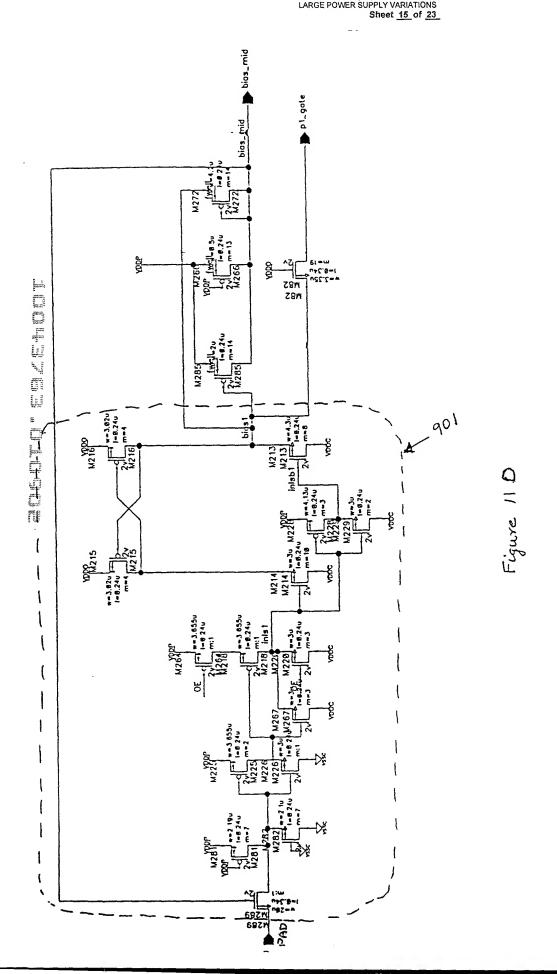


Figure 11C

Attorney: Nicholas J. Pauley
Docket No.: 41980/NJP/B600
Inventor(s): Janardhanan S Ajit
Title: SUB-MICRON HIGH+NPUT VOLTAGE TOLERANT
INPUT OUTPUT (I/O) CIRCUIT WHICH ACCOMMODATES
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Attorney: Nicholas J. Pauley

Docket No.: 41980/NJP/B600

Inventor(s): Janardhanan S. Ajit

Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT
INPUT OUTPUT (I/O) CIRCUIT WHICH ACCOMMODATES
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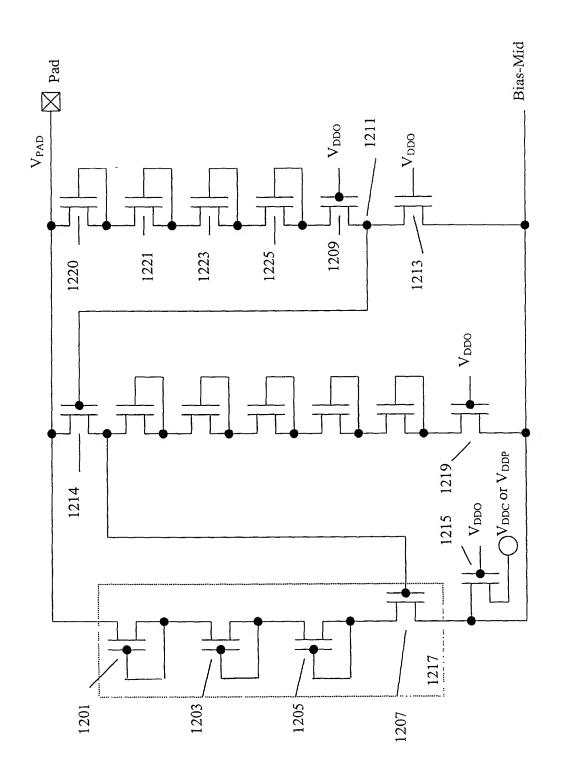
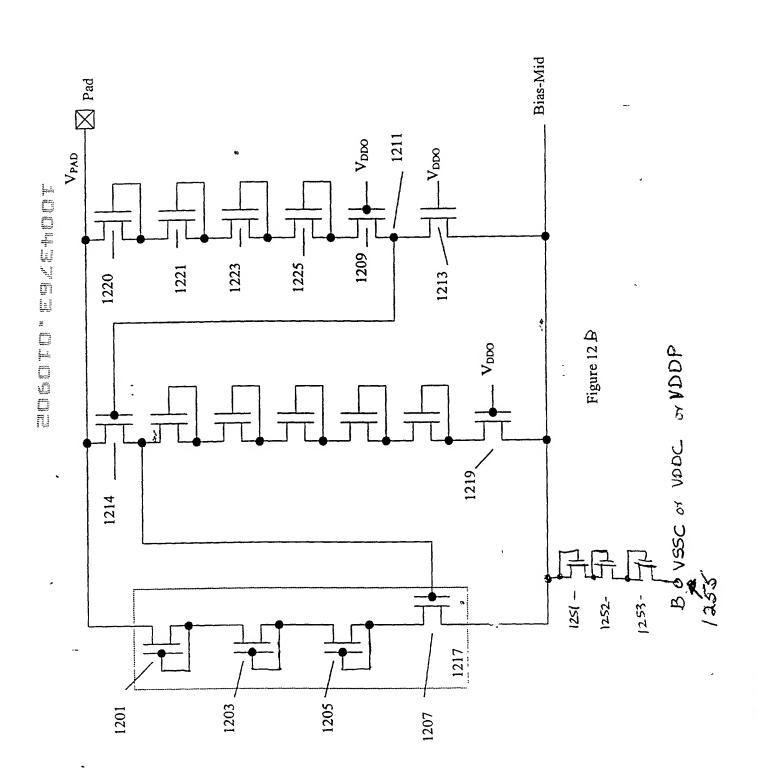


Figure 12₽

Attorney: Nicholas J. Pauley
Docket No.: 41980/NJP/B600
Inventor(s): Janardhanan S Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT
INPUT OUTPUT (I/O) CIRCUIT WHICH ACCOMMODATES
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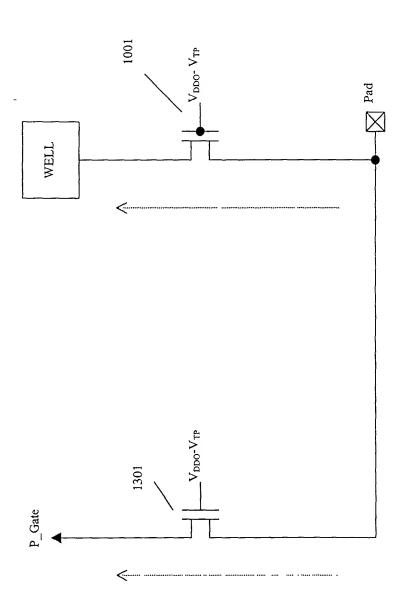


Attorney: Nicholas J. Pauley

Docket No.: 41980/NJP/B600

Inventor(s): Janardhanan S. Ajit

Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT
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Attorney: Nicholas J. Pauley
Docket No.: 41980/NJP/B600
Inventor(s): Janardhanan S Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT
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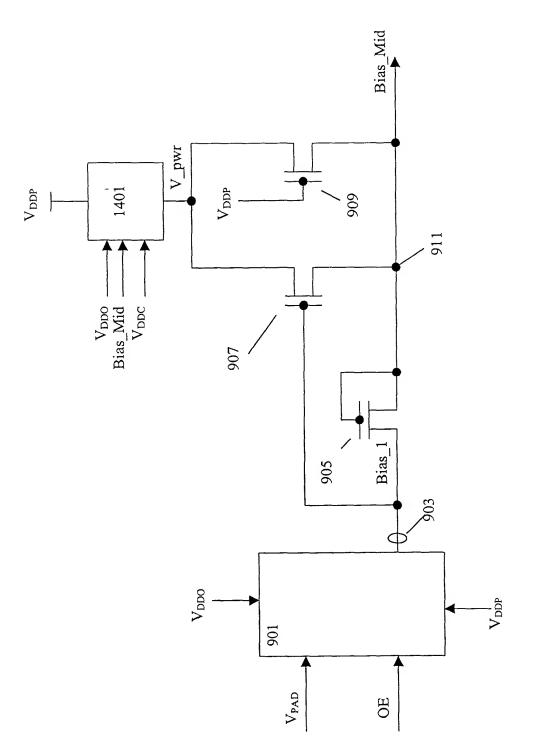


Figure 14

Attorney: Nicholas J. Pauley
Docket No.: 41980/NJP/B600
Inventor(s): Janardhanan S. Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT
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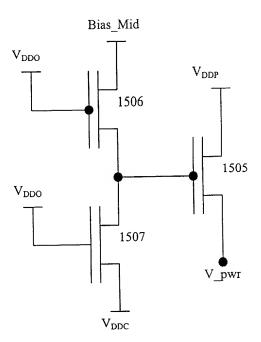


Figure 15

Attorney: Nicholas J. Pauley
Docket No.: 41980/NJP/B600
Inventor(s): Janardhanan S. Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT
INPUT OUTPUT (I/O) CIRCUIT WHICH ACCOMMODATES
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Attorney: Nicholas J. Pauley
Docket No.: 41980/NJP/B600
Inventor(s): Janardhanań S Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT
INPUT OUTPUT (I/O) CIRCUIT WHICH ACCOMMODATES
LARGE POWER SUPPLY VARIATIONS

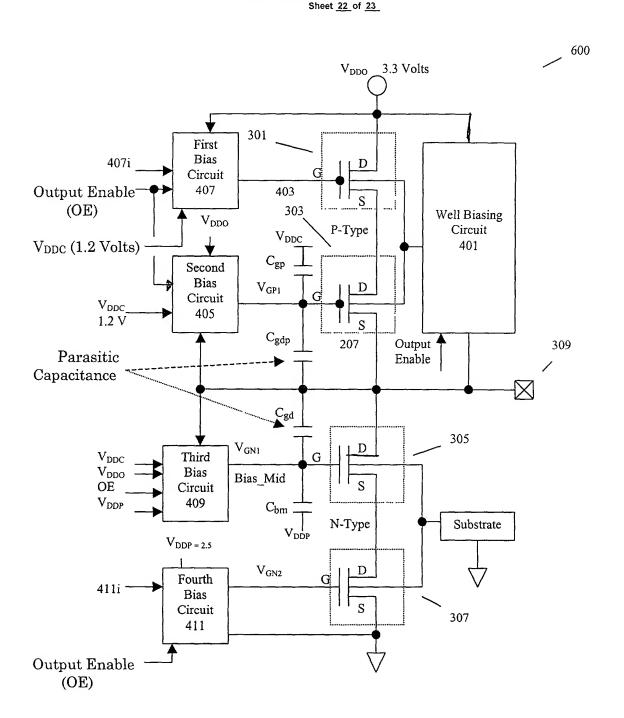


Figure 17

Attorney: Nicholas J. Pauley
Docket No.: 41980/NJP/B600
Inventor(s): Janardhanan S. Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT
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VDDO NDDO 407 301 PRE-DRIVER DATA WELL FOR DRIVER PMOS OE VDDC VDDO VDDC VOPL 303 -1220 VODO LALISON T Saay NOOD NOON OUR 305 901 Bias_Mid 905 -909c -910c NOOC VDDO -VDDP PRE-DRIVER €-307 DATA -FOR DRIVER NMOS OE 411 ♥ VSSC VSSO

FLAURE 18